YAMAGUCHI Q66494 IMAGE DISPLAY METHOD AND IMAGE DISPLAY APPARATUS Filed November 21, 2001 Danyl Mexic 202-293-7060 1 of 4

8bit, 1bit × 8 VIDEO CARD STEP NUMBER 9.5bit CONVERTING IMAGE PROCESSING-SECTION

FIG.2 18R 32a 12 30 1,8 34 R (8bit) -ADDER 1pix (8bit) ADDER 2pix (8bit) 1bit ∠3<u>6</u> → 3pix (8bit) G 1bit 1bit clock 38-1bit ADDER ► 1pix (8bit) B (8bit) -**ADDER** → 2pix (8bit) clock -401 ~ 3pix (8bit) R (8bit) r clock 32b 18L 18R 18 LCD LCD PANEL

FIG.3

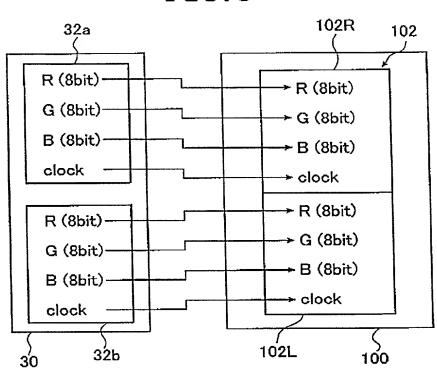


FIG.4

